



Position: Senior / IC Design Engineer – Analog Design

Location: Hong Kong / Nanjing / Shenzhen / Taiwan

Job Responsibilities:

- Perform analog and mixed-signal circuit design and system integration;
- Supervise physical layout and perform design verifications like pre-layout simulation, post-layout simulation, co-simulation;
- Support behavior modeling;
- Participate in design review and engage in design flow / methodology improvement;
- Compile design documentation and integration guide;
- Define the test plan and perform the silicon evaluation.

Job Requirements:

- Bachelor Degree or above in Electronic Engineering or equivalent;
- 5 years design experience and knowledge in analog circuits;
- Applicant with less experience will be considered as analog design engineer;
- Experience in the following areas will be advantageous: -
 - * Mixed signal SoC;
 - * AMS or VerilogA verification.

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by E-mail:

recruit@solomon-systech.com

All applications will be treated in strict confidence and for recruitment purpose only.