



Position: IC Design Engineer - ESD

Location: Taiwan

工作內容:

- 負責晶片產品的 ESD 設計、測試晶片的 IP 設計和 ESD 版圖設計監督工作;
- 負責晶片 ESD 和 TLP 測試、產品調試及產品 ESD 失效性能分析;
- 記錄 ESD 測試計劃、設計規則和電性報告;
- 建立 ESD IP 資料庫和技術基準比對。

職位要求：

- 大學以上電子電機科系畢業;
- 4 年以上 ESD 晶片 IO 設計經驗;
- 熟悉半導體電路元件、具晶圓製程經驗者更佳。

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by one of the following channels. Please quote our reference number in your application.

1. Mail: Human Resources Department, Solomon Systech Limited, 6/F, No. 3 Science Park East Avenue, Hong Kong Science Park, Shatin, N.T., or
2. E-mail: recruit@solomon-systech.com
3. Upload the resume directly by clicking the apply button on each job opening

We regret that only short-listed candidates will be notified.