Position: ASIC Design Head

Location: Hong Kong / Shenzhen / Taiwan

Job Responsibilities:

- Ensure complete device integration, including analogue, digital IPs, memories, IOs and passives as required;
- Execute the development per aggressive timelines / quality targets;
- Conduct full IC specification to Final Netlist activities, including ownership for the validation/characterization upon silicon arrival;
- Define the implementation and simulation strategies for optimal Mixed Signal IC execution (Design and Integration);
- Design from initial written specification or 'C' model to final DFT inserted gate level RTL creation, Synthesis, insertion of Test logic, Clocking and Debug structures;
- Implement simulation test bench for designs and carry out the simulations ensuring high levels of coverage across analogue and digital IPs;
- Execute formal verification between the various compiled views (including RTL and gate level) deriving specification from system level documents Utilizing system 'c' or 'e' language modelling and verification;
- Scope includes silicon, packaging and industrialization, primary interface for the IC design activity.

Job Requirements:

- Bachelor or above in Electronics Engineering or equivalent;
- 10 years of solid experience in Engineering;
- Familiar with digital / mixed-mode Simulation / verification;
- Familiar with Baseband signal processing or DSP architecture;
- Expertise in digital design flow, including SystemVerilog, Synthesis, DFT etc;
- Working knowledge of Mixed Signal design for different interface design;
- Detailed knowledge of design for manufacturing and reliability best practices;
- Excellent configuration management expertise.

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by E-mail: recruit@solomon-systech.com

All applications will be treated in strict confidence and for recruitment purpose only.