



**Position: IC Design Engineer – Mask Preparation and Physical Design Automation**

**Location: Hong Kong**

**Job Responsibilities:**

- Manage IC database tape out work with foundries, mask shops and assembly sites;
- Perform mask data integrity check through Job Deck View (JDV) platform;
- Write necessary programs or scripts to enhance work efficiency;
- Maintain foundries' process/design kit and system data by healthy checking and performance turning;
- Perform system data backup, restore and application migration control;
- Perform full and semi-custom IC layout construction and review using various IC layout and verification tools.

**Job Requirements:**

- Higher Diploma or Bachelor in Electronic Engineering or equivalent;
- With 2 to 3 years of relevant working experience is preferable;
- Knowledge in CMOS IC physical layout design and able to use various EDA tools;
- Good understanding of semiconductor theory, structures of devices and fabrication process is an advantage;
- Knowledge in TCL/TK, MySQL, JAVA, HTML or VB is an advantage;
- Good communication skills with English and Mandarin;
- Self-motivated with good working attitude;
- Fresh graduates will also be considered.

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by E-mail: [recruit@solomon-systech.com](mailto:recruit@solomon-systech.com)

All applications will be treated in strict confidence and for recruitment purpose only.