Position: Senior / Digital IC Design Engineer

Location: Hong Kong

Job Responsibilities:

- Perform logic design, RTL coding, design verification, logic synthesis, DFT and static timing analysis;
- Develop verification environment and coverage closure;
- Compile design documentation and integration guide;
- Support co-simulation;
- Support wafer level testing and silicon evaluation.

Job Requirements:

- B.Sc or above in Electronic Engineering or equivalent;
- 5 Years or above of solid experience in one or more of the following areas: Verilog-based logic design and synthesis, constrained random testbench with System Verilog & UVM, assertion based design verification or circuit-level SPICE simulations;
- Knowledge of SoC and embedded system;
- Candidate with less experience will be considered as Digital Design Engineer.

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by E-mail: recruit@solomon-systech.com