



Position: Senior Engineer – Analog/Digital IC Design

Location: Hong Kong

Job Responsibilities:

- Conduct research on new technology which can be implemented into our R&D projects;
- Prepare and maintain the testchip database, and analyze the data;
- Conduct experiment and testing;
- Perform analog/mixed-signal circuit/digital design and system integration;
- Participate in design review and engage in design flow / methodology improvement;
- Compile test report.

Job Requirements:

- Bachelor Degree or above in Electronic Engineering or equivalent;
- 5 years design experience and knowledge in analog circuits;
- Applicant will be considered as **Engineer or Engineering Manager** of either Analog or Digital IC Design in accordance to the experience;
- Experience in the following areas will be advantageous: Mixed signal SoC / AMS or VerilogA verification / FPGA design and implementation / Circuit-level SPICE simulations / Verilog-based logic design and synthesis.

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by E-mail: recruit@solomon-systech.com