



Position : Physical Design Engineer

Location : Taiwan

Job Responsibilities:

- Communicate with engineers to provide solutions on verification area;
- Learning for new verification tool and develop related deck;
- Performing layout of custom analog circuit blocks.

Job Requirements:

- Bachelor Degree in Engineering or equivalent;
- 2 years relevant work experience;
- Knowledge in CMOS physical layout design;
- Good at using various EDA tools.

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by E-mail: recruit@solomon-systech.com