

Part Number	SSD1905	SSD1906	SSD1918
Embedded Display Buffer Memory	80KB	256KB	174KB
MCU Interface			
- Control pins	Enhanced 6800, 8080, generic #1, generic #2, control interface, w/ wait option		6800, 8080, 3/4 wire SPI, w/ wait option
- Data bus	8 or 16 bit options		8, 9, 16 or 18 bit options
- Address bus	Separated bus required		Not required, internal auto increment
Panels Support			
Main			
- Panel types	STN or TFT (mono or color) Mono, Grey Scale, Color (STN) 4-bit / 8-bit driver bus (STN) 9-bit, 12-bit or 18-bit digital RGB (TFT, HR-TFT, AD-TFT)		Generic TFT panel 9-bit, 12-bit or 18-bit digital RGB
- Color depth	1/2/4/8/16 bits-per-pixel (bpp)		8/16/18 bpp
- Resolution	Programmable according to frame buffer memory		Within 320x240 (QVGA)
Sub	NONE (Only 1 main panel supported)		Almost any driver controllers w/ 8 bit or 9 bit 6800, 8080 or 3/4 wires SPI; separated programmable RESET pin available
Display Features			
- Display rotation	√		√
- Display color invert	√		√
- Multi-page buffering	√		2 pages (QCIF+), 1 page (QVGA)
- Horizontal Resolution doubling	-		X
- Color look-up-table	√		X
- Floating window	√		X
- Virtual display	√		X
- Hardware cursors	2		0
Power			
- MCU interface	3.0V - 3.6V		1.6V - 3.6V
- Main panel interface	3.0V - 3.6V		1.6V - 3.6V
- Sub-panel interface	N.A.		1.6V - 3.6V
- Core voltage	2.5V (by internal regulator)		1.8V (selectable internal regulator)
- Power consumption	~ 2mW to 50mW depends on screen, color and resolution		~ 2mW to 6mW typically
Misc			
- GPIO pins	√		√
- PWM and CV	√		√
- Frame synchronisation signal to MCU	-		√
- Package	TQFP-100	TQFP-100 / TFBGA-100	Gold bump die / LGA-112