



Position: Senior / Digital IC Design Engineer

Location: Hong Kong

Job Responsibilities:

- Perform logic design, RTL coding, design verification, logic synthesis, DFT and static timing analysis
- Develop verification environment and coverage closure
- Support wafer level testing and silicon evaluation

Job Requirements:

- B.Sc or above in Electronic Engineering or equivalent
- 5 Years or above of solid experience in one or more of the following areas: Verilog-based logic design and synthesis, constrained random testbench with System Verilog & UVM, assertion based design verification or circuit-level SPICE simulations
- Knowledge of SoC and embedded system.
- Candidate with less experience will be considered as Digital Design Engineer

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by one of the following channels. Please quote our reference number in your application.

1. Mail: Human Resources Department, Solomon Systech Limited, 6/F, No. 3 Science Park East Avenue, Hong Kong Science Park, Shatin, N.T., or
2. E-mail: recruit@solomon-systech.com, or
3. Upload the resume directly by clicking the apply button on each job opening

We regret that only short-listed candidates will be notified.