



**職位：Analog Design Engineer - EPD/PMOLED**

**工作地點：台灣**

**職位描述：**

- 執行類比及混合信號系統/包括設計輸入、電路模擬及系統級模擬的電路設計。
- 為晶片測試評估開發測試模組。
- 執行設備/電路特性、晶片及產品的評估。
- 致力於智慧財產權和產品/工藝過程開發。
- 執行文檔及設計評審。

**職位要求：**

- 碩士以上學歷，電子電機工程或相關專業。
- 具有類比 IC 設計工作經驗,有 EPD/PMOLED 設計工作經驗的優先考慮。
- 熟悉 Verilog/Verilog AMS 的優先考慮。

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by one of the following channels. Please quote our reference number in your application.

1. Mail: Human Resources Department, Solomon Systech Limited, 6/F, No. 3 Science Park East Avenue, Hong Kong Science Park, Shatin, N.T., or
2. E-mail: [recruit@solomon-systech.com](mailto:recruit@solomon-systech.com)
3. Upload the resume directly by clicking the apply button on each job opening

We regret that only short-listed candidates will be notified.